Overview of the Course



Copyright 2010, Keith D. Cooper & Linda Torczon, all rights reserved.

Students enrolled in Comp 412 at Rice University have explicit permission to make copies of these materials for their personal use.

Faculty from other educational institutions may use these materials for nonprofit educational purposes, provided this copyright notice is preserved.

The organization of this course

Schedule :

Two weekly lectures

			Roderta Gori
Mar	11:00	12:45	roberta.gori@di.unipi.it
Gio	9:00	10:45	<u>rober ra.gor real.unipi.rr</u>

One weekly lecture for constructing a compiler

Ven 11:00 12:4



Dela sut a Caut

Letterio Galletta <u>letterio.galletta@imtlucca.it</u>

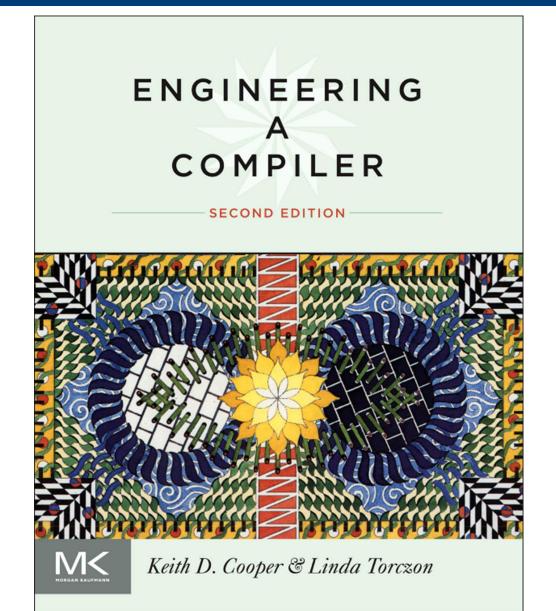
What we will see

A brief recall on formal languages:

•Grammars, automata, theorems, regular and context free languages

- •Chomsky hierarchy
- Lexical analysis
- Parser
- Contextual analysis
- Intermediate representation
- Code shape
- Optimization
- Dataflow analysis
- More static analyses: control flow and abstract interpretation
- Register allocation

Our textbook



• web page, I will add there all the slides

<u>www.di.unipi.it/~gori/Linguaggi-Compilatori2020</u> Material for specific topics:

- Introduction to Automata Theory, Languages, And Computation. Hopcroft, Motwani, Ullman
- Fondamenti dell'Informatica. Linguaggi formali, calcolabilita' e complessita'. Dovier, Giacobazzi Bollati Boringhieri
- Principles of Program Analysis. Nielson,Nielson, Hankin Springer
- <u>Static Inference of Numeric Invariants by Abstract Interpretation</u> a tutorial by Antoine Mine on Abstract interpretation.

Roberta Gori <u>roberta.gori@di.unipi.it</u>

My own research program

- Whole program analysis for verification and optimization
- Static analysis to discern program behavior
- Abstract interpretation based techniques

Compilers

- What is a compiler?
 - A program that translates an executable program in one language into an executable program in another language
 - The compiler should improve the program, in some way
- What is an interpreter?
 - A program that reads an executable program and an input and produces the results of executing that program on the input
- C and C++ are typically compiled,

Pyton and Scheme are typically interpreted

- Java is complicated
 - Compiled to bytecodes (code for the Java VM)
 - which are then interpreted
 - Or a hybrid strategy is used
 → Just-in-time compilation

Common mis-statement: X is an interpreted language (or a compiled language) It's a property of the implementation !

Why Study Compilation?

- Compilers are important
 - Responsible for many aspects of system performance
 - Attaining performance has become more difficult over time
 - → In 1980, typical code got 85% or more of peak performance
 - \rightarrow Today, that number is closer to 5 to 10% of peak
 - -> Compiler has become a prime determiner of performance
- Compilers are interesting
 - Compilers include many applications of theory to practice
 - Writing a compiler exposes algorithmic & engineering issues
- Compilers are everywhere
 - Many practical applications have embedded languages

 \rightarrow Commands, macros, formatting tags ...

- Many applications have input formats that look like languages

Still many open problems!

Reducing the Price of Abstraction

- Computer Science is the art of creating virtual objects and making them useful.
- We invent abstractions and uses for them
- We invent ways to make them efficient
- Programming is the way we realize these inventions

Well written compilers make abstraction affordable

- Cost of executing code should reflect the underlying work rather than the way the programmer chose to write it
- Change in expression should bring small performance change
- Cannot expect compiler to devise better algorithms
 - Don't expect bubblesort to become quicksort

It was our belief that if FORTRAN, during its first months, were to translate any reasonable "scientific" source program into an object program only half as fast as its hand-coded counterpart, then acceptance of our system would be in serious danger... I believe that if we failed to produce efficient programs, the widespread use of languages like FORTRAN would have been seriously delayed.

- John Backus on the subject of the 1st FORTRAN compiler

Era nostra convinzione che se FORTRAN, durante i suoi primi mesi, avesse tradotto un qualsiasi programma sorgente "scientifico" ragionevole in un programma oggetto piu' efficiente solo della metà della sua controparte codificata a mano, allora l'accettazione del nostro linguaggio sarebbe stata in serio pericolo. Credo che se non fossimo riusciti a produrre programmi efficienti, l'uso diffuso di linguaggi come FORTRAN sarebbe stato seriamente ritardato. All data collected with gcc 4.1, -O3, running on a queiscent, multiuser Intel T9600 @ 2.8 GHz

Simple Examples

Which is faster?

```
for (i=0; i<n; i++)
for (j=0; j<n; j++)
A[i][j] = 0;
```

```
for (i=0; i<n; i++)
for (j=0; j<n; j++)
A[j][i] = 0;
```

All three loops have distinct performance.

0.51 sec on 10,000 × 10,000 array

 $1.65 \text{ sec} \text{ on } 10,000 \times 10,000 \text{ array}$

```
p = &A[0][0];
t = n * n;
for (i=0; i<t; i++)
*p++ = 0;
```

0.11 sec on 10,000 x 10,000 array

A good compiler should know these tradeoffs, on each target, and generate the best code. Few real compilers do.

Conventional wisdom suggests using

```
bzero((void*) &A[0][0],(size_t) n*n*sizeof(int))
```

> Compiler construction poses challenging and interesting problems:

- Compilers must process large inputs, perform complex algorithms, but also run quickly
- Compilers have primary responsibility for run-time performance
- Compilers are responsible for making it acceptable to use the full power of the programming language
- Computer architects perpetually create new challenges for the compiler by building more complex machines

→ Compilers must hide that complexity from the programmer

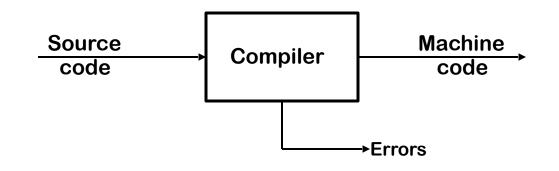
A successful compiler requires mastery of the many complex interactions between its constituent parts

Intrinsic Interest

Compiler construction involves ideas from many different parts of computer science

Artificial intelligence	Greedy algorithms Heuristic search techniques
Algorithms	Graph algorithms, union-find Dynamic programming
Theory	DFAs & PDAs, pattern matching Fixed-point algorithms
Systems	Allocation & naming, Synchronization, locality
Architecture	Pipeline & hierarchy management Instruction set use

The View from 35,000 Feet

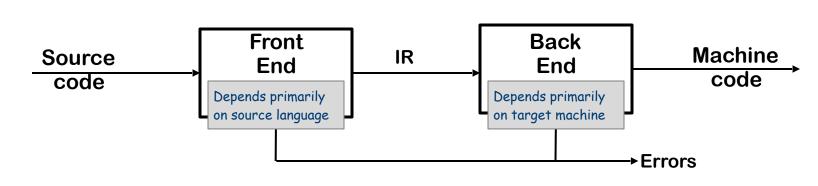


Implications

- Must recognize legal (and illegal) programs
- Must generate correct code
- Must manage storage of all variables (and code)
- Must agree with OS & linker on format for object code

Big step up from assembly language—use higher level notations

Traditional Two-pass Compiler



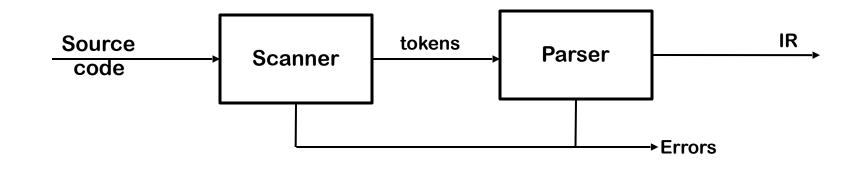
Implications

- Use an intermediate representation (IR)
- Front end maps legal source code into IR
- Back end maps IR into target machine code
- Admits multiple front ends & multiple passes

Classic principle from software engineering: Separation of concerns

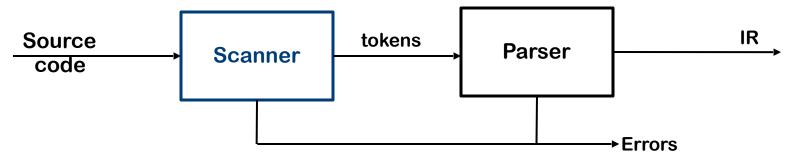
(better code)

Typically, front end is O(n) or O(n log n), while back end is NPC



Responsibilities

- Recognize legal (& illegal) programs
- Report errors in a useful way
- Produce IR & preliminary storage map
- Shape the code for the rest of the compiler
- Much of front end construction can be automated



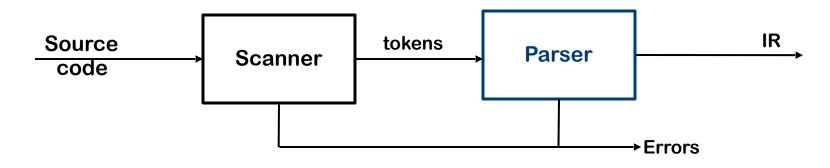
Scanner

- Maps character stream into words—the basic unit of syntax (Lexical analysis)
- Produces pairs a word & its part of speech
 - x = x + y; becomes $\langle id, x \rangle = \langle id, x \rangle + \langle id, y \rangle$;
 - word \cong lexeme, part of speech \cong token type, pair \cong a token
- Typical tokens include number, identifier, +, -, new, while, if
- Speed is important

Textbooks advocate automatic scanner generation Commercial practice appears to be hand-coded scanners Split program to individual words that makes sense: My mother cooooookes dinner not.

> while (y < z) { int x = a + b; y += x; }

T While T LeftParen T Identifier y T Less T Identifier z T RightParen T OpenBrace T Int T Identifier x T Assign T Identifier a T Plus T Identifier b T Semicolon T Identifier y T PlusAssign T Identifier x T Semicolon T CloseBrace



Parser

- Recognizes context-free syntax & reports errors (Syntax Analysis)
- Guides context-sensitive ("semantic") analysis (type checking)
- Builds IR for source program

Hand-coded parsers are fairly easy to build Most books advocate using automatic parser generators For Lexical and Syntact analysis we need grammars

```
SheepNoise \rightarrow SheepNoise <u>baa</u>
                             baa
```

This grammar defines the set of noises that a sheep makes under normal circumstances

It is written in a variant of Backus-Naur Form (BNF)

Formally, a grammar G = (S,N,T,P)

- S is the start symbol
- N is a set of non-terminal symbols
- T is a set of terminal symbols or words
- P is a set of productions or rewrite rules $(P: N \rightarrow N \cup T)$

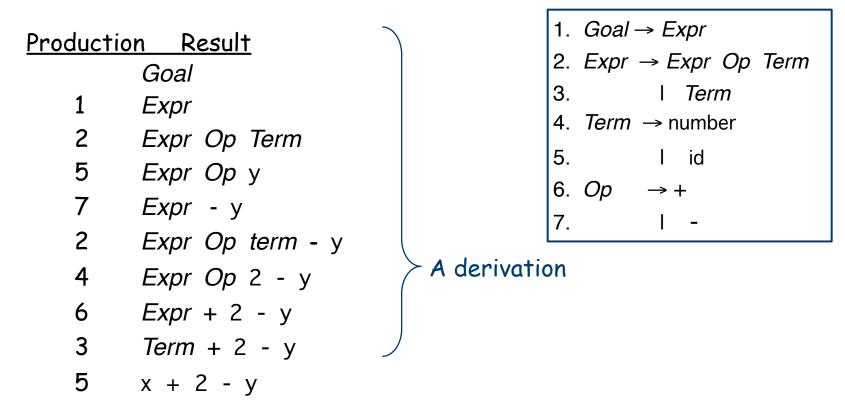
Context-free syntax can be put to better use

1. Goal → Expr
2. Expr → Expr Op Term
3. <i>I Term</i>
4 <i>. Term</i> → number
5 <i>. l</i> id
6. <i>Op</i> → +
7. / -

S = Goal T = { <u>number</u>, <u>id</u>, +, - } N = { *Goal*, *Expr*, *Term*, *Op* } P = { 1, 2, 3, 4, 5, 6, 7 }

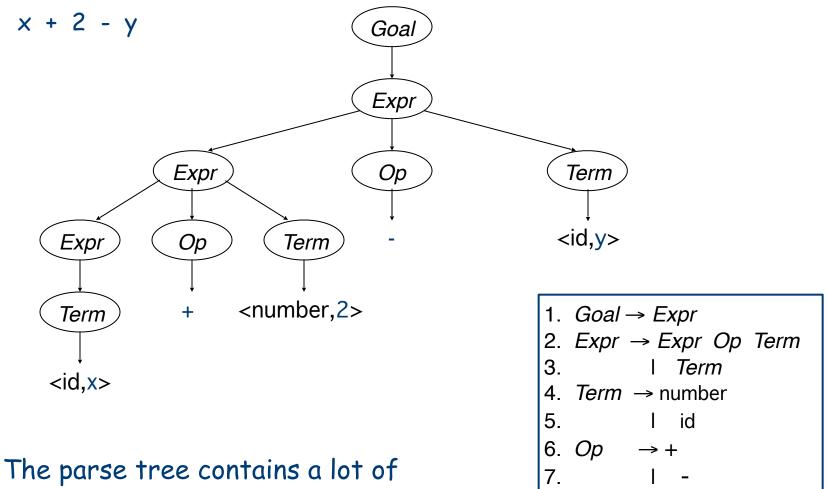
- This grammar defines simple expressions with addition & subtraction over "number" and "id"
- This grammar, like many, falls in a class called "context-free grammars", abbreviated CFG

Given a CFG, we can derive sentences by repeated substitution



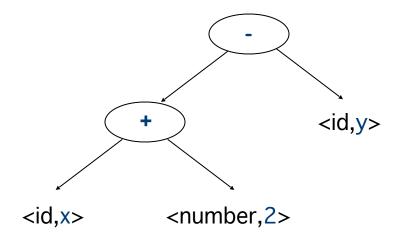
To recognize a valid sentence in some CFG, we reverse this process and build up a parse

A parse can be represented by a tree (parse tree or syntax tree)



unneeded information

Compilers often use an abstract syntax tree instead of a parse tree

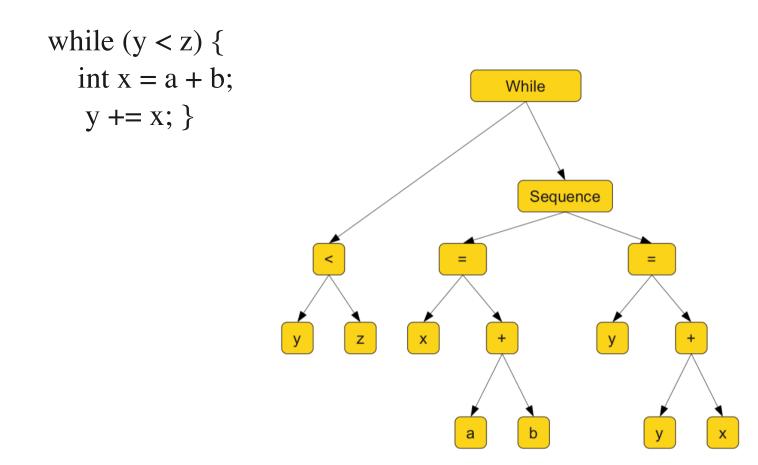


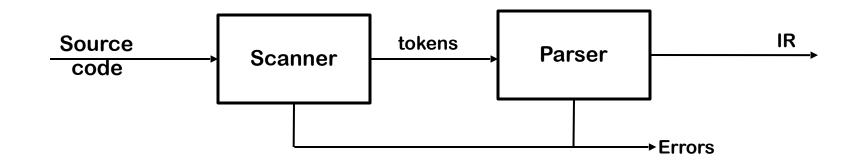
The AST summarizes grammatical structure, without including detail about the derivation

This is much more concise

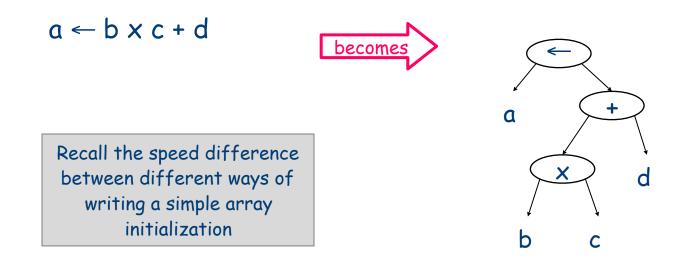
ASTs are one kind of intermediate representation (IR)

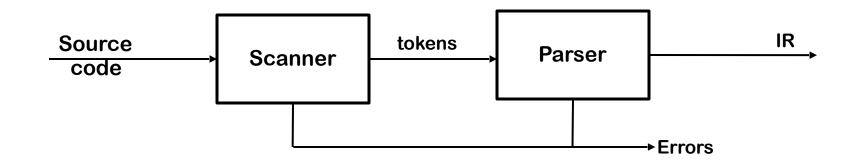
Split program to individual words that makes sense: My mother cookes dinner not.



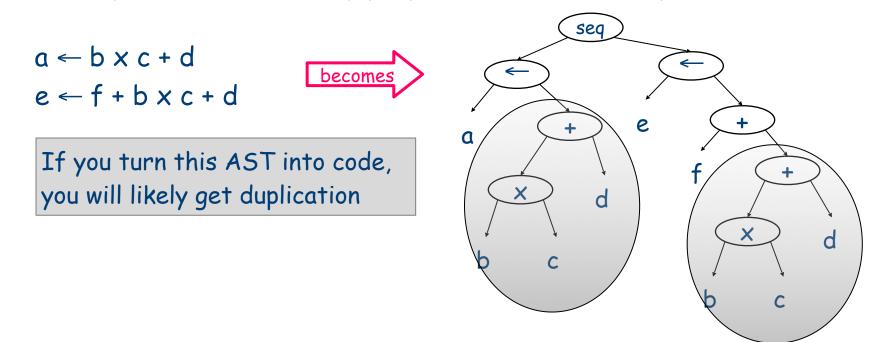


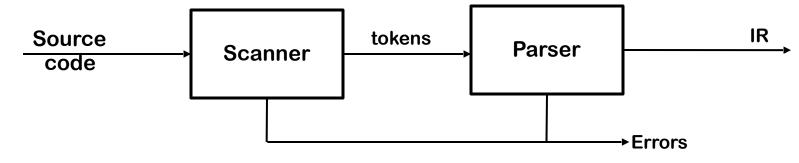
Code shape determines many properties of resulting program





Code shape determines many properties of resulting program





Code shape determines many properties of resulting program

becomes

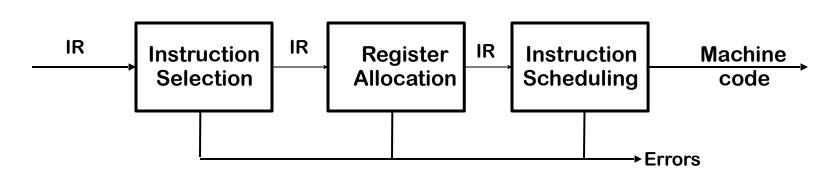
$$a \leftarrow b \times c + d$$

$$e \leftarrow f + b \times c + d$$

We would like to produce this code, but getting it right takes a fair amount of effort

$$\begin{array}{c} \text{load } \textcircled{@b} \Rightarrow r_1 \\ \text{load } \textcircled{@c} \Rightarrow r_2 \\ \text{mult } r_1, r_2 \Rightarrow r_3 \\ \text{load } \textcircled{@d} \Rightarrow r_4 \\ \text{add } r_3, r_4 \Rightarrow r_5 \\ \text{store } r_5 \Rightarrow \textcircled{@a} \\ \text{load } \textcircled{@f} \Rightarrow r_6 \\ \text{add } r_5, r_6 \Rightarrow r_7 \\ \text{store } r_7 \Rightarrow \textcircled{@e} \\ \end{array} \right) \begin{array}{c} \text{computes} \\ \text{b x c + d} \\ \text{computes} \\ \text{computes} \\ \text{b x c + d} \\ \text{computes} \\ \text{computes} \\ \text{b x c + d} \\ \text{computes} \\ \text{computes} \\ \text{b x c + d} \\ \text{computes} \\ \text{computes} \\ \text{b x c + d} \\ \text{computes} \\ \text{computes} \\ \text{computes} \\ \text{b x c + d} \\ \text{computes} \\ \text{computes}$$

The Back End

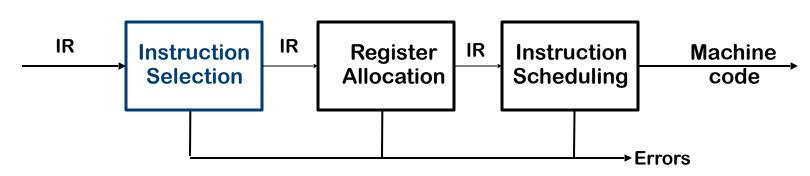


Responsibilities

- Translate IR into target machine code
- Choose instructions to implement each IR operation
- Decide which value to keep in registers

Automation has been less successful in the back end

The Back End



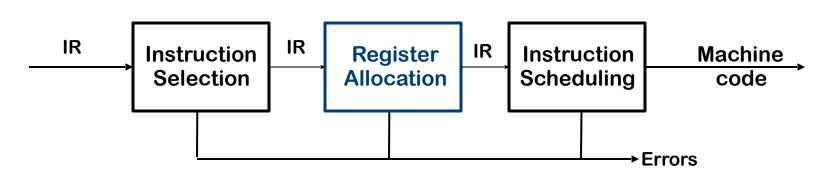
Instruction Selection

- Produce fast, compact code
- Take advantage of target features such as addressing modes
- Usually viewed as a pattern matching problem
 - ad hoc methods, pattern matching, dynamic programming
 - Form of the IR influences choice of technique

This was the problem of the future in 1978

- Spurred by transition from PDP-11 to VAX-11
- Orthogonality of RISC simplified this problem

The Back End



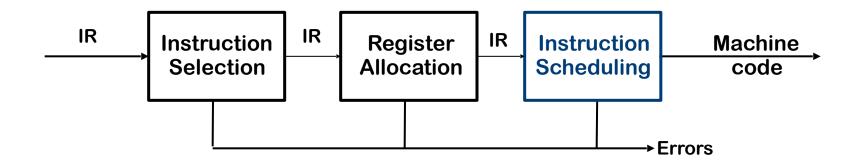
Register Allocation

- Have each value in a register when it is used
- Manage a limited set of resources
- Can change instruction choices & insert LOADs & STORES
- Optimal allocation is NP-Complete in most settings

Compilers approximate solutions to NP-Complete problems

Register allocation for $a = (a \times 2 \times b \times c) \times d$

Use 6 registers!	<pre>loadAI r_{arp}, @a \Rightarrow r_a loadI 2 \Rightarrow r_2 loadAI r_{arp}, @b \Rightarrow r_b loadAI r_{arp}, @c \Rightarrow r_c loadAI r_{arp}, @d \Rightarrow r_d mult r_a, r_2 \Rightarrow r_a mult r_a, r_b \Rightarrow r_a mult r_a, r_c \Rightarrow r_a mult r_a, r_d \Rightarrow r_a storeAI r_a \Rightarrow r_{arp}, @a</pre>	<pre>// load 'a' // constant 2 into r_2 // load 'b' // load 'c' // load 'd' // $r_a \leftarrow a \times 2$ // $r_a \leftarrow (a \times 2) \times b$ // $r_a \leftarrow (a \times 2 \times b) \times c$ // $r_a \leftarrow (a \times 2 \times b \times c) \times d$ // write r_a back to 'a'</pre>
$\begin{array}{llllllllllllllllllllllllllllllllllll$	<pre>// load 'a' // $r_1 \leftarrow a \times 2$ // load 'b' // $r_1 \leftarrow (a \times 2) \times b$ // load 'c' // $r_1 \leftarrow (a \times 2 \times b) \times c$ // load 'd' // $r_1 \leftarrow (a \times 2 \times b \times c) \times d$ // write r_a back to 'a'</pre>	Use 3 registers!



Instruction Scheduling

- Avoid hardware stalls and interlocks
- Use all functional units productively
- Can increase lifetime of variables (changing the allocation)

Optimal scheduling is NP-Complete in nearly all cases

Heuristic techniques are well developed

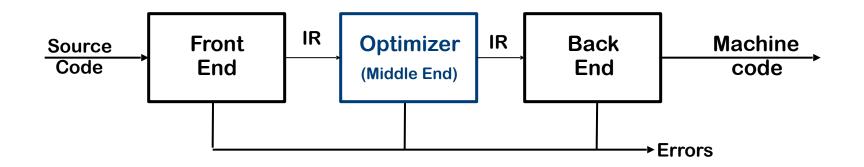
Instruction scheduling

 Reorder operations to reflect the target machine performance constraints

Start	End					
1	3	loadAI	r _{arp} ,@a	\Rightarrow r ₁	//load'a'	
4	4	add	r ₁ , r ₁	\Rightarrow r ₁	∥r ₁ ← a × 2	
5	7	loadAI	r _{arp} ,@b	$\Rightarrow r_2$	//load'b'	
8	9	mult	r ₁ , r ₂	\Rightarrow r ₁	$// r_1 \leftarrow (a \times 2)$)×b
10	12	loadAI	r _{arp} ,@c	\Rightarrow r ₂	//load'c'	
13	14	mult	r ₁ , r ₂	\Rightarrow r ₁	$// r_1 \leftarrow (a \times 2)$	×b)×c
15	17	loadAI	r _{arp} ,@d	$\Rightarrow r_2$	//load'd'	
18	19	mult	r ₁ , r ₂	\Rightarrow r ₁	$// r_1 \leftarrow (a \times 2)$	×b×c)×d
20	22	storeAI	r ₁	\Rightarrow r $_{arp}$, @a	∥write r _a bac	ck to 'a'
						LoadAI, sta

LoadAI, storeAI 3 cycles mult 2 cycles others 1 cycle

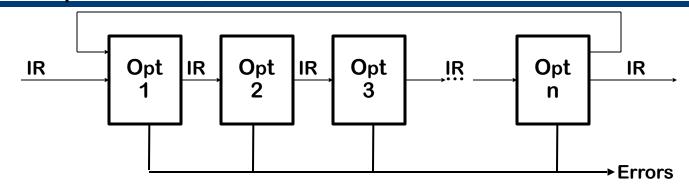
Traditional Three-part Compiler



Code Improvement (or Optimization)

- Analyzes IR and rewrites (or <u>transforms</u>) IR
- Primary goal is to reduce running time of the compiled code
 May also improve space, power consumption, ...
- Must preserve "meaning" of the code
 - Measured by values of named variables

The Optimizer (or Middle End)



Modern optimizers are structured as a series of passes

Typical Transformations

- Discover & propagate some constant value
- Move a computation to a less frequently executed place
- Specialize some computation based on context
- Discover a redundant computation & remove it
- Remove useless or unreachable code
- Encode an idiom in some particularly efficient form